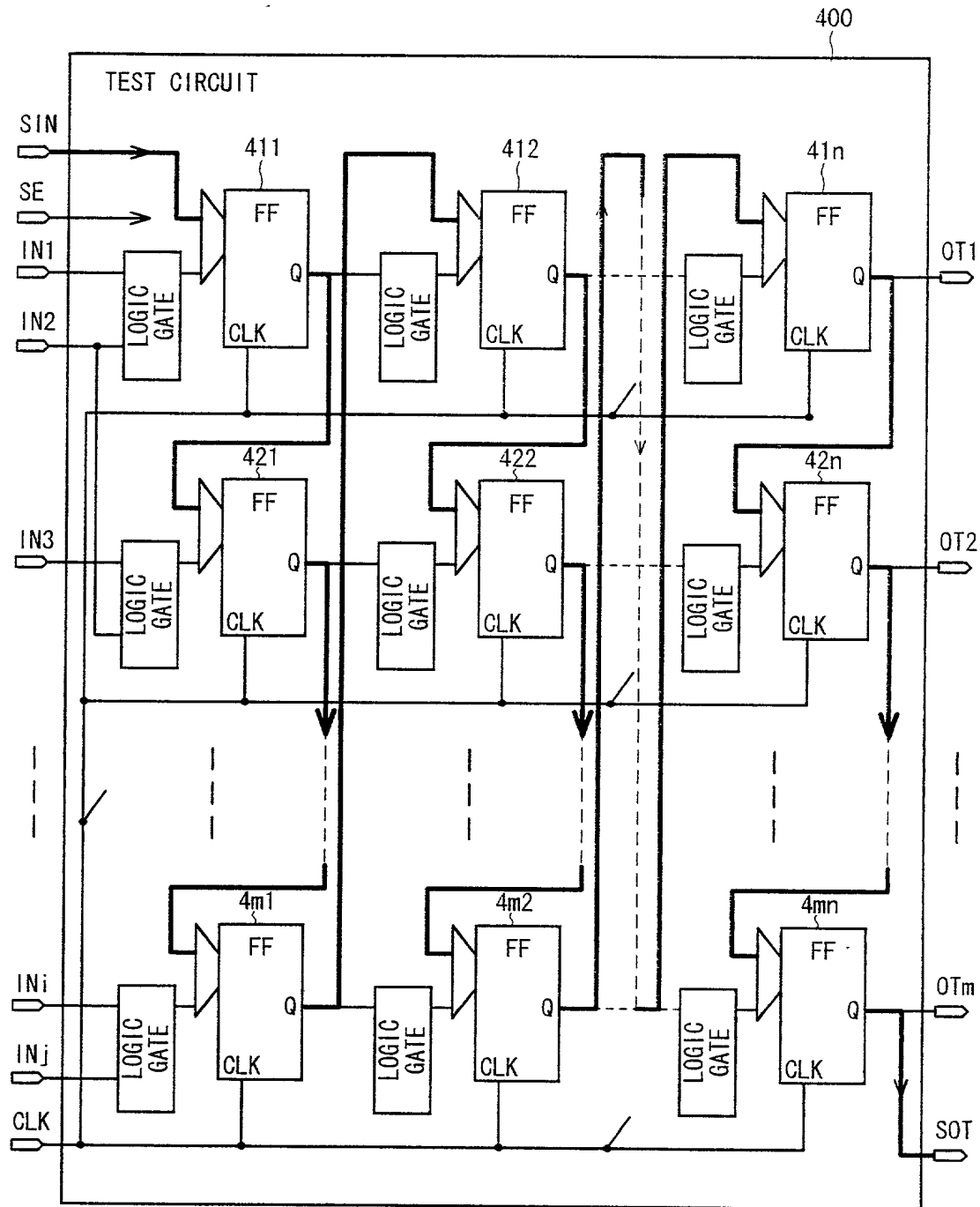


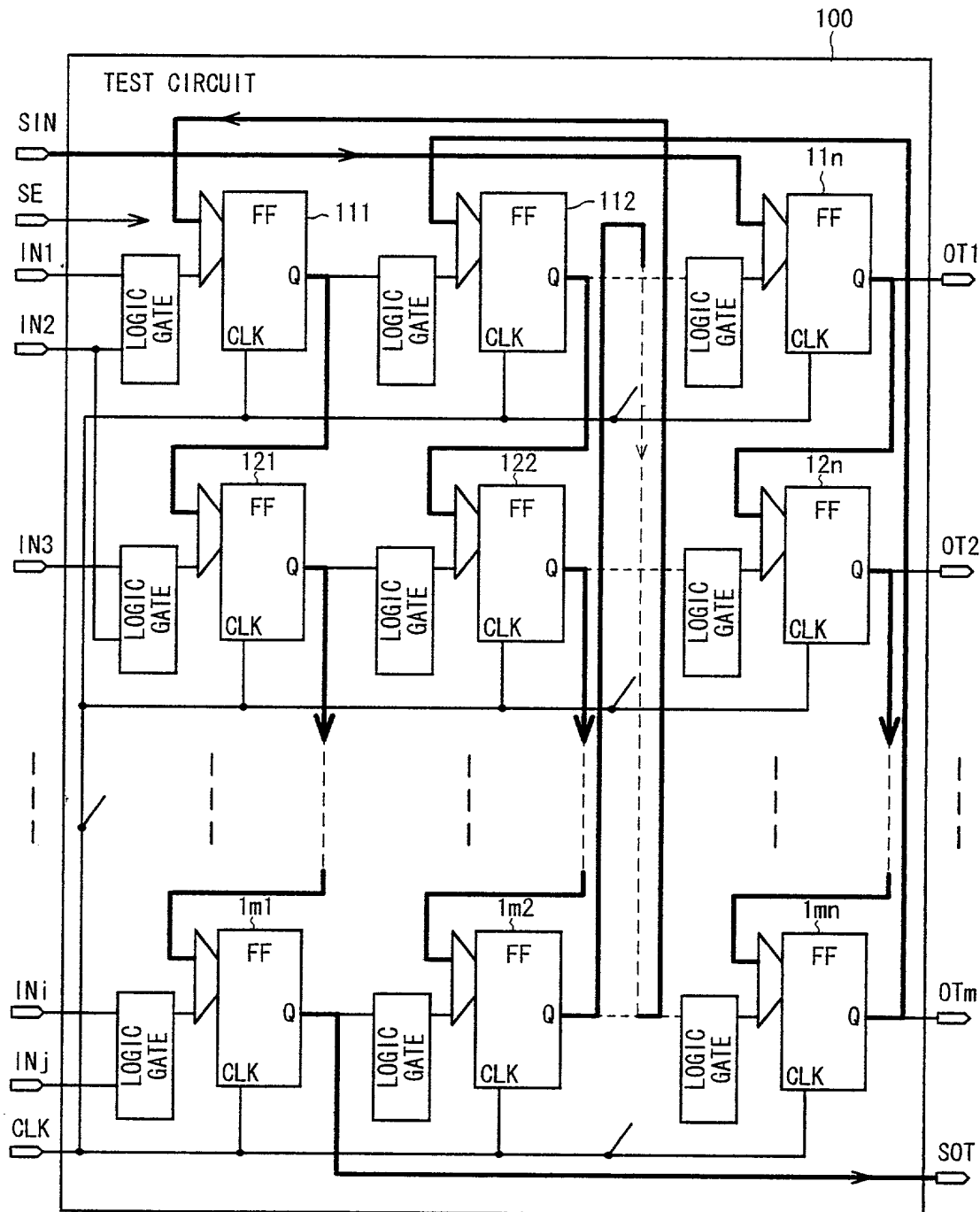
FIG. 1 PRIOR ART



IN1 TO IN_j: INPUT TERMINALS
 OT1 TO OT_m: OUTPUT TERMINALS
 SIN: SCAN INPUT
 SOT: SCAN OUTPUT

SE: SCAN ENABLE TERMINAL
 CLK: CLOCK SIGNAL INPUT TERMINAL
 FF: FLIP-FLOP

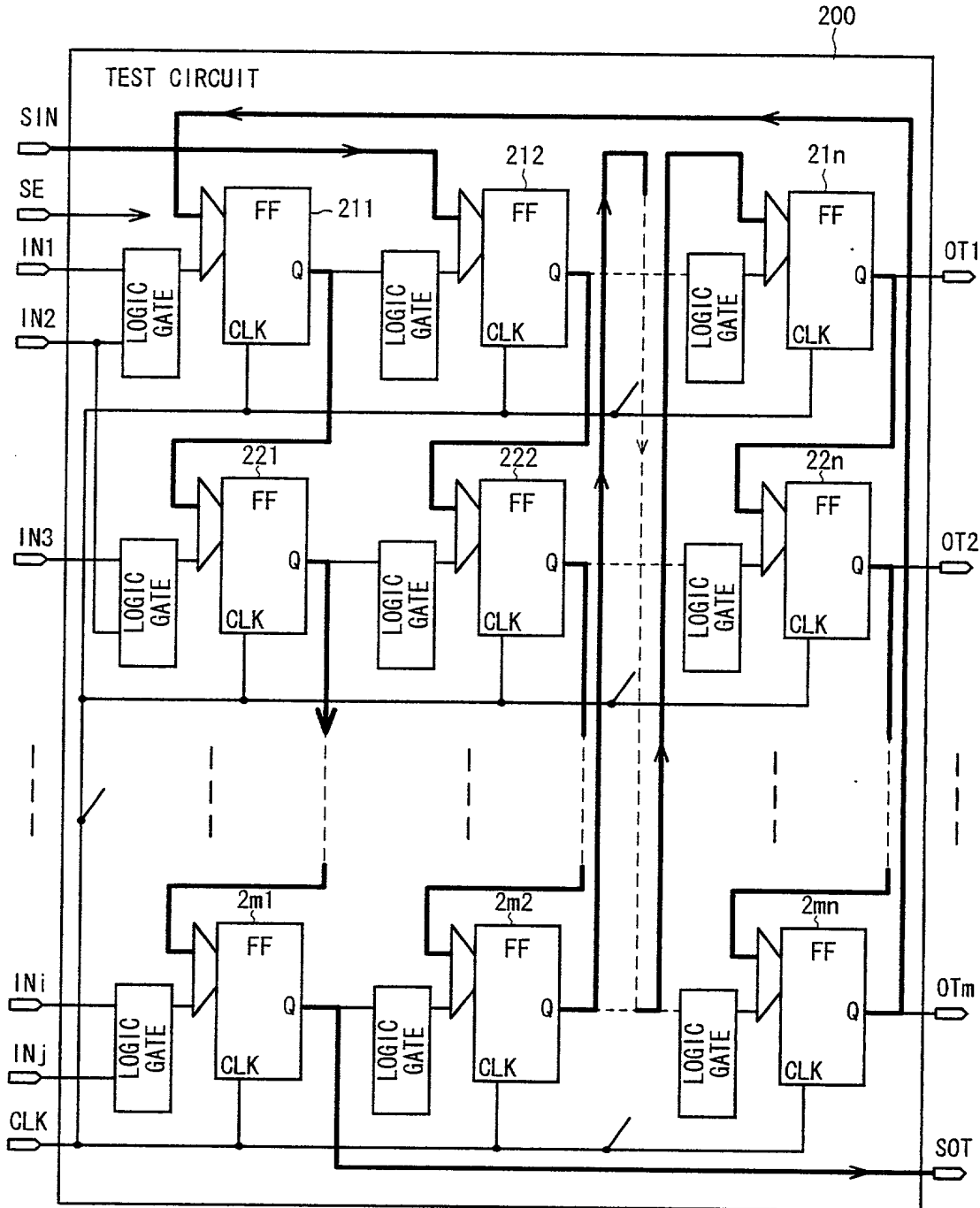
FIG. 3



IN1 TO INj: INPUT TERMINALS
OT1 TO OTm: OUTPUT TERMINALS
SIN: SCAN INPUT
SOT: SCAN OUTPUT

SE: SCAN ENABLE TERMINAL
CLK: CLOCK SIGNAL INPUT TERMINAL
FF: FLIP-FLOP

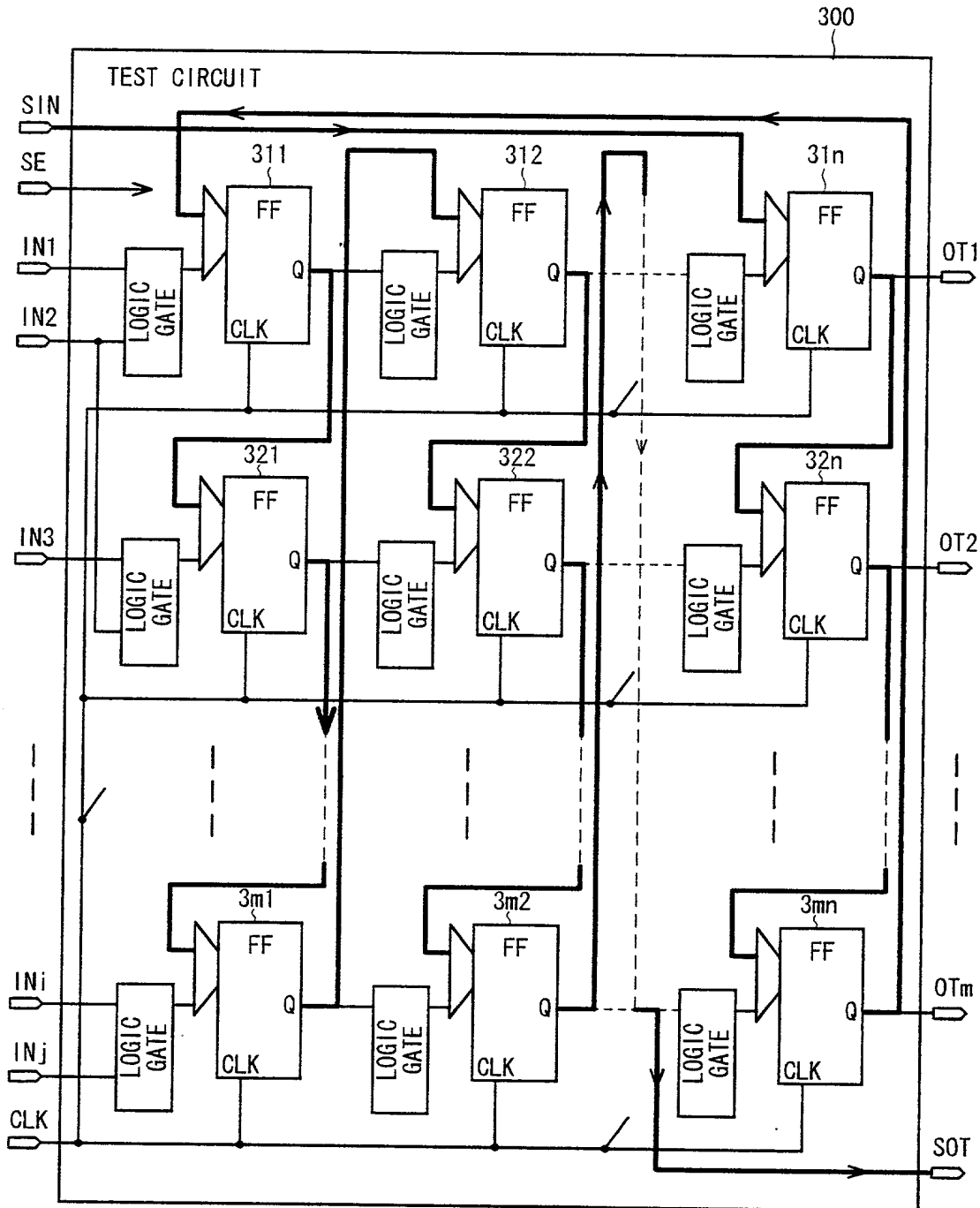
FIG. 4



IN1 TO INj: INPUT TERMINALS
OT1 TO OTm: OUTPUT TERMINALS
SIN: SCAN INPUT
SOT: SCAN OUTPUT

SE: SCAN ENABLE TERMINAL
CLK: CLOCK SIGNAL INPUT TERMINAL
FF: FLIP-FLOP

FIG. 5



IN1 TO INj: INPUT TERMINALS
 OT1 TO OTm: OUTPUT TERMINALS
 SIN: SCAN INPUT
 SOT: SCAN OUTPUT

SE: SCAN ENABLE TERMINAL
 CLK: CLOCK SIGNAL INPUT TERMINAL
 FF: FLIP-FLOP